Dep-TEE: Decoupled Memory Protection for Secure and Scalable Inter-enclave Communication on RISC-V

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ABSTRACT

Trusted Execution Environment (TEE) has been widely implemented by modern hardware vendors to protect security and privacy-sensitive applications and data, such as Intel SGX/TDX, ARM TrustZone, AMD SEV, and RISC-V Penglai. However, existing TEE systems face challenges in balancing memory isolation among security, performance, and scalability requirements. This paper introduces a novel TEE system, Dep-TEE, which decouples memory protection (to segments) from address translation (to page tables). This design improves communication performance by dynamically adjusting memory protection capabilities, without sacrificing application compatibility, and enhances security by safeguarding against attacks on page tables. We have built a prototype of Dep-TEE based on FPGA, incorporating hardware extensions and software support. The evaluation demonstrates that Dep-TEE significantly surpasses existing TEE solutions, achieving three orders of magnitude lower communication latency and 10x greater scalability while maintaining robust security guarantees.

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1 INTRODUCTION

Trusted Execution Environments (TEEs) serve as secure computing environments, offering a hardware/software co-design protection solution to safeguard sensitive data and applications in cloud environments [\[3,](#page-6-1) [12,](#page-6-2) [13\]](#page-6-3). Enclaves, as crucial security containers within TEEs, protect critical parts of applications from malicious attacks and software vulnerabilities.

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In the cloud environment, enclaves often collaborate to manage distributed computing tasks or share sensitive data. Consequently, secure and efficient inter-enclave communication is essential for completing various complex operations. For instance, multiple hospitals might share patient data to train a machine-learning model for disease diagnosis, enhancing the model's performance [\[25\]](#page-6-4). Consider a scenario where a service provider rents cloud computing machines with Intel SGX [\[16\]](#page-6-5) to provide dedicated enclave processes for each client [\[1\]](#page-6-6). However, the limited secure memory (PRM) in SGX leads to significant overhead due to multiple copying and encryption/decryption operations during inter-enclave communication. While Elasticlave [\[26\]](#page-6-7), based on the Keystone framework [\[15\]](#page-6-8), supports inter-enclave communication, its scalability is hindered by hardware limitations, making it unsuitable for cloud environments. Therefore, improving the scalability and performance of inter-enclave communication while ensuring data security is of significance.

We analyzed existing TEE communication mechanisms, categorizing them into copy+encryption, remapping, and shared memory. Theoretical and experimental analyses show that the shared memory mechanism offers the best performance but introduces scalability and security issues. Existing TEE systems fail to meet the demands of scalability, security, and high performance for interenclave communication. To address these challenges, we propose a novel TEE communication system called Dep-TEE, which utilizes a hardware-level Supervisor Physical Memory Protection (SPMP) mechanism [\[19\]](#page-6-9) and shared memory. Our main contributions are summarized as follows:

- We design a novel TEE communication system named Dep-TEE. Dep-TEE utilizes the shared memory mechanism to achieve inter-enclave communication. It exploits the SPMP mechanism to decouple permission protection from enclaves' page tables, achieving high security and scalability while not compromising shared memory's high performance.
- We implement a prototype of Dep-TEE on the Penglai-based platform and integrate it into the high-performant opensource RISC-V processor core, Xiangshan Nanhu Core. After introducing the SPMP mechanism, Dep-TEE shows negligible overhead in CPU-intensive RV8 benchmarks, demonstrating high-performance efficiency. The implementation on the Xilinx VU19P FPGA shows that Dep-TEE requires minimal extra hardware resources, utilizing only 0.2% of LUTs and 0.04% of FFs.
- System evaluation shows that Dep-TEE can securely and efficiently support communication among at least 100 pairs of enclaves. Compared to the Penglai-PMP system, Dep-TEE

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Table 1: A comparison of TEE communication mechanisms. Isolation means the isolation mechanism among enclaves. Unrestricted means the number of enclaves is unrestricted, though system performance may decline if secure memory is insufficient.Performance is indicated by arrows: \uparrow (high), \downarrow (low), and \rightarrow (medium). R. and T. denote the security risks from Rowhammer and TOCTTOU attacks. Dep-TEE uniquely offers scalable, high-performance, and secure inter-enclave communication.

shows performance improvements of one to three orders of magnitude across various data transfer sizes.

We organize this paper as follows. First, Section [2](#page-1-0) introduces the communication mechanisms in existing TEE systems and their limitations. Section [3](#page-2-0) overviews the Dep-TEE framework and details the design. Section [4](#page-4-0) details the Dep-TEE implementation. Section [5](#page-4-1) presents the system evaluation and secure analysis. Finally, Section [6](#page-5-0) concludes this work.

2 BACKGROUND AND MOTIVATION

2.1 Inter-Enclave Communication Mechanisms

Inter-process communication (IPC) is crucial in modern OSes for better modularity and is increasingly important for TEE systems. It facilitates functions decomposition and collaborative work, supports complex application scenarios, and enables multitasking [\[1,](#page-6-6) [12,](#page-6-2) [25\]](#page-6-4). A secure, efficient, and scalable communication mechanism is crucial in cloud computing, but current inter-enclave communication schemes fall short.

Existing inter-enclave communication mechanisms include copy+ encryption, remapping, and shared memory, as summarized in Table [1.](#page-1-1) TEE systems like Intel SGX [\[16\]](#page-6-5), RISC-V Sanctum [\[7\]](#page-6-13), and Penglai-PMP [\[18\]](#page-6-15) use the copy+encryption mechanism, where data is encrypted, transmitted to untrusted memory by sender enclave, and then decrypted by the receiver enclave. This mechanism ensures data confidentiality and integrity but incurs high overhead. ARM Komodo [\[10\]](#page-6-11) and RISC-V Penglai-TVM [\[9\]](#page-6-14) adopt the remapping mechanism, where enclaves unmap and remap physical memory for communication. This mechanism is inefficient due to its coarse granularity (4KB pages) and TLB shootdown issues. Intel TDX [\[13\]](#page-6-3), AMD SEV [\[3\]](#page-6-1), and ARM CCA [\[4\]](#page-6-10) map shared memory via page tables in a virtual machine, avoiding performance loss from copying and remapping but leaving page tables vulnerable to attacks like Rowhammer [\[2\]](#page-6-16) and TOCTTOU [\[8\]](#page-6-17). High-concurrency demands in cloud environments amplify these security risks. Elasticlave [\[26\]](#page-6-7), based on Keystone [\[15\]](#page-6-8) and RISC-V PMP [\[22\]](#page-6-18), improves security by isolating shared memory but struggles with high-concurrency communication due to resource constraints.

2.2 Inter-Enclave Communication Patterns

2.2.1 Bilateral Communication Patterns. We apply the communication mechanisms summarized in [§2.1](#page-1-2) to three representative

bilateral data sharing patterns in real-world scenarios as proposed in [\[26\]](#page-6-7), shown in Figure [1.](#page-2-1)

Pattern 1: Producer-Consumer. In this pattern, the producer enclave transfers communication data to the consumer enclave (Figure [1\(](#page-2-1)a)). Common application's sub-steps often use this pattern, such as batch processing in web frameworks [\[11,](#page-6-19) [21\]](#page-6-20).

Latency tests (Figure [2\)](#page-2-2) using varying record sizes show that the copy+encryption mechanism has the highest overhead due to extensive copying and encryption/decryption. The remapping mechanism has moderate overhead from re-mapping and un-mapping operations, triggering TLB shootdown issues. The shared memory mechanism has the lowest overhead, with consistent instruction level and read/write operations regardless of data size.

Pattern 2: Client-Server. The client and the server enclaves may simultaneously read from and write to shared data (Figure [1\(](#page-2-1)b)). The copy+encryption mechanism involves four copying and two encryption/decryption processes, whereas the shared memory mechanism requires only two mapping operations.

Pattern 3: Proxy. A proxy enclave processes data from a source enclave and forwards it to a destination enclave (Figure [1\(](#page-2-1)c)) like a caching proxy in web services (e.g., Nginx [\[17\]](#page-6-21)).

2.2.2 Multilateral Communication Patterns. We propose two multilateral communication patterns: multi-bilateral communication and single-producer multi-consumer.

Pattern 4: Multi-Bilateral Communication. In cloud environments, numerous enclaves must operate concurrently, using bilateral communication patterns for multitasking. TEE systems must support this concurrency while ensuring high performance and security.

Pattern 5: Single-Producer Multi-Consumer. A producer enclave processes data and transmits it to multiple consumer enclaves. For instance, a healthcare firm can provide personalized recommendations based on customer-provided information, such as medical histories [\[1\]](#page-6-6). This pattern enables multiple clients to query a shared database without revealing personal health details.

2.3 Challenges

Existing TEE systems still fall short of satisfying the inter-enclave communication versatility, i.e., scalability, high performance, and high security. We use the shared memory mechanism to achieve the versatility goal. Specifically, we should overcome three challenges. Dep-TEE: Decoupled Memory Protection for Secure and Scalable Inter-enclave Communication on RISC-V ASPDAC '25, January 20–23, 2025, Tokyo, Japan

Figure 1: In-depth analysis of implementation processes for three distinct inter-enclave communication mechanisms across various bilateral communication patterns.

Challenge-1: Scalable communication over shared memory. Despite using shared memory for inter-enclave communication, Elasticlave's scalability is limited by its use of the Keystone framework and RISC-V PMP for memory isolation. The number of PMP registers limits Elasticlave's concurrency capability for enclaves.

Challenge-2: Performance over shared memory. Intel TDX, AMD SEV and ARM CCA employ page tables for memory isolation within virtual machines, enabling high-concurrency inter-enclave communication through the shared memory mechanism. However, this mechanism is vulnerable to TOCTTOU attacks, which are hard to mitigate. Additionally, these TEE systems require multiple memory accesses to check the permission table, reducing the performance.

Challenge-3: Security over shared memory. Many TEE systems including RISC-V Penglai-TVM, use page tables for isolation to support concurrent enclaves. While page tables serve for address translation and permission protection, they are vulnerable to targeted attacks like Rowhammer. Existing security mechanisms for page tables often incur substantial performance costs.

Our key insight to resolve the above challenges is decoupling protection (based on segment) from sharing (based on page tables). First, the decoupling can achieve high security without compromising performance as Dep-TEE only relies on registerbased segments for security protection, which can defend against attackers targeting page tables (e.g., Rowhammer). Second, we further introduce a layered segment design that can achieve scalable segments compared with existing single-layer designs.

3 DEP-TEE DESIGN

3.1 Architecture

We highlight two key aspects of the Dep-TEE abstraction: (1) memory isolation with decoupled protection and (2) versatile interenclave communication. We integrate these abstractions into a secure monitor operating in the highest privilege mode (e.g., machine mode in RISC-V), as shown in Figure [3.](#page-3-0) The secure monitor manages all enclaves and provides APIs for users to deploy enclaves. It is loaded and verified by the boot ROM during system startup.

We introduce the RISC-V SPMP (Supervisor Physical Memory Protection) mechanism [\[19\]](#page-6-9) to assist the secure monitor in implementing the Dep-TEE's abstractions. We use the SPMP mechanism to decouple the permission protection from the page tables, defending against specific attacks targeting the page table. Additionally, we leverage SPMP registers to isolate shared memory for versatile inter-enclave communication.

Figure 2: Breakdowns of three communication mechanisms in the producer-consumer pattern. Data labels in the diagram are scaled proportionally to actual data, measured in cycles.

Threat Model. The trusted computing base (TCB) of Dep-TEE includes the hardware CPU and the secure monitor. We assume attackers can access detailed information about OS, hardware components, and network configuration, and can create and execute malicious enclaves. Attackers may exploit OS vulnerabilities or use malicious enclaves to gain unauthorized access and manipulate messages between the CPU and hardware. Moreover, attackers could leverage TOCTTOU attacks and targeting page tables attacks (e.g. Rowhammer) to extract sensitive data. Side-channel and denial-of-service (DoS) attacks are not considered in this work.

3.2 Isolation with Decoupled Protection

Scalability Challenges. RISC-V-based TEEs like Keystone and Penglai-PMP use Physical Memory Protection (PMP) to isolate enclaves. PMP has 16 sets of address and configuration registers in Machine Mode (M-mode) that define physical memory's size, location, and access permissions. Memory access is granted only if PMP checks pass; otherwise, access is denied. Each enclave's isolation requires a dedicated set of PMP registers, but the limited number of PMP registers restricts the concurrent enclaves. This limitation poses a scalability challenge for RISC-V-based TEEs in cloud environments that require handling many tasks simultaneously.

Table-based Memory Isolation. Within a TEE region isolated by PMP registers, we use a page table mechanism to isolate among enclaves, as shown in Figure [4.](#page-3-1) Each enclave's page tables, stored in its dedicated memory, are configured by the secure monitor to map to its memory space. This approach supports the simultaneous creation of multiple enclaves within the same TEE region. In the SV39 page table structure, hardware memory access requires four references: three for the page table pages and one for the data page. Each access involves a PMP check to verify permission. However, this approach is vulnerable to attacks targeting page tables like

(a) Architecture of Dep-TEE. Dep-TEE provides versatile inter-enclave communication and decoupled protection over SOTA TEE systems like Penglai. (b) Inter-enclave communication process based on the client-server pattern. er.SPMP and ee.SPMP mean the SPMP registers context of enclaver and enclavee, respectively. Figure 3: Overview of the Dep-TEE architecture with a detailed depiction of the inter-enclave communication process.

Figure 4: Isolation and access checks after decoupling permission protection from page tables in the TEE region.

Rowhammer, which can leak sensitive data. To counter this, we introduce the RISC-V SPMP mechanism to decouple permission protection from page tables, enhancing the isolation and protection of enclaves within the TEE region.

RISC-V SPMP Mechanism. SPMP is a segment-based design similar to RISC-V PMP. It supports 16 entries, each consisting of an address register and a configuration register. The SPMP entries can define the range of physical memory regions managed, with the range limited by the A field in the configuration register and the address register. The access permissions for these regions are determined by the X (execute), W (write), and R (read) fields in the configuration register. The S field of the configuration marks a rule as S-mode-only when set and U-mode-only when unset.

TEE Region Memory Management. When creating an enclave within a TEE region, the secure monitor allocates a contiguous block of physical memory, establishes the page tables, and assigns metadata containing the enclave's information. This metadata, stored in an isolated memory area managed by the secure monitor, includes the enclave's unique identifier (eid), starting physical address, memory size, SPMP register context, and more. During the enclave creation, the secure monitor writes the configuration information into the SPMP context based on the allocated physical address, size, and access permissions. When the enclave is executed, the secure monitor uses the SPMP context to write into the SPMP address and configuration registers via the CSR.write instruction, ensuring the correct memory access permissions. When switching to another enclave, the secure monitor retrieves the target enclave's SPMP context based on its eid and loads it into the SPMP registers. This dynamic adjustment of SPMP register configurations ensures strict isolation among different enclaves. Thus, within the TEE region, the management and switching of the SPMP register context effectively facilitate the physical memory isolation of multiple enclaves.

By using segment-based SPMP registers, we decouple permission protection from page tables, establishing a secure and scalable

isolation mechanism for enclaves. As shown in Figure [4,](#page-3-1) the system performs an SPMP check alongside each PMP check, allowing memory access only if both checks pass. This approach enhances security and efficiency by storing permission settings directly in registers and conducting checks internally within the CPU.

3.3 Versatile Inter-Enclave Communication

Communication Interface. In our system, the enclave initiating communication is the enclaver, and the enclave receiving communication is the enclavee. Table [2](#page-4-2) outlines the seven instructions for versatile inter-enclave communication within Dep-TEE. In this scheme, the key is predefined by the user-defined communication schemes and includes two parts: shm_key and enclave_key. The shm_key is used to connect enclavee to shared memory, while the enclave_key ensures the enclavee is correctly identified when the enclaver transfers shared memory permissions. This paper details the design and implementation of this inter-enclave communication mechanism through a client-server communication flow.

Communication Flow. A predefined key is bound to the eid during the creation of each enclave requiring communication. For enclaves communicating via the same shared memory, their shm keys are identical, while *enclave* keys are unique. Figure [3\(](#page-3-0)b) shows the communication flow between two enclaves in the TEE region using a client-server pattern and SPMP registers' changes at each stage.

Stage 1: The enclaver issuing the create instruction to request the shared memory. The secure monitor allocates the memory and binds its shmid with the shm_key and the enclaver's eid, and updates the SPMP registers and the enclaver's SPMP context. Upon successfully creating shared memory, the enclavee obtains the shmid via shm key and associates the memory using the attach instruction.

Stage 2: Both enclaves execute the map instruction, mapping the shared memory into their virtual address spaces. The enclaver has read and write access, but the enclavee cannot access the memory because the SPMP registers store the information of the enclaver.

Stage 3: After writing data, enclaver transfers access permissions of the shared memory to the enclavee using the transfer instruction. The secure monitor updates the SPMP registers, allowing the enclavee to read the request (req) and provide a response (resp).

Stage 4: After sending its resp, the enclavee transfers access permissions back to the enclaver using the transfer instruction.

Stage 5: When communication concludes, the enclavee uses the detach instruction to disassociate from the shared memory, and Dep-TEE: Decoupled Memory Protection for Secure and Scalable Inter-enclave Communication on RISC-V ASPDAC '25, January 20–23, 2025, Tokyo, Japan

Figure 5: Dep-TEE implementation based on Xiangshan Nanhu Core. The red box highlights the areas most closely associated with the PMP and SPMP checker.

the enclaver may destroy the shared memory using the destroy instruction. The secure monitor clears the SPMP configurations and memory mappings, releasing resources.

The bilateral and multi-bilateral communication patterns in [§2.2](#page-1-3) all follow this fundamental flow. By using the share instruction, the enclaver can set the shared memory to read-only, allowing the enclavees executing the attach instruction to read data, enabling a single-producer multiple-consumer communication pattern.

Ownership Transfer. In this scheme, the transfer instruction triggers a trap into the M-mode secure monitor, which reconfigures the SPMP registers to facilitate shared memory ownership transfer among enclaves. This approach avoids the overhead of multiple un-mappings and re-mappings needed in a remapping mechanism and enhances security against TOCTTOU attacks and targeting page table attacks.

4 IMPLEMENTATION

Hardware. We extend the SOTA RISC-V high-performance processor Xiangshan Nanhu-V2 processor (an 11-level superscalar out-oforder core) [\[24\]](#page-6-22) by adding 16 SPMP CSR registers and introducing an SPMP Checker module, as shown in the Figure [5.](#page-4-3) During memory access, the MMU translates virtual addresses into physical addresses, performing parallel PMP and SPMP checks. Only if both checks pass memory access is allowed; otherwise, it is prohibited. These modifications do not alter the core's pipelines.

Software. We implement Dep-TEE on the Penglai Enclave (PMP version, v0.2 release [\[18\]](#page-6-15)), an advanced RISC-V platform open-source TEE. Penglai Enclave provides Linux drivers, an SDK, and authentication mechanisms for creating, running, relaying, and destroying enclaves. We extend the secure monitor to support scalable and

efficient inter-enclave communication and TEE region memory management, without using a guarded page table proposed by Penglai-TVM or other new hardware features.

Methodology. We evaluate Dep-TEE on the Xilinx Virtex Ultra-Scale+ VU19P (XCVU19P) FPGA which emulates a Nanhu Core SoC operating at 2GHz. Detailed hardware configurations are in Table [3.](#page-4-4) Microbenchmark tests compare Dep-TEE with Penglai-Copy (based on the original PMP) and Penglai-Remapping (PMP version with a remapping communication mechanism). Application-level benchmarks compare Dep-TEE with Penglai-PMP.

5 EVALUATION

5.1 Microbenchmarks

To evaluate Dep-TEE and the Penglai baseline (PL-Copy and PL-Remapping), we develop a benchmark encompassing five communication patterns based on three sharing mechanisms. Our research focuses on data transmission performance, excluding data processing procedures. Specifically, we analyze data transfer in Dep-TEE and Penglai-Remapping and data copying in Penglai-Copy, omitting encryption or decryption operations.

Performance of Bilateral Communication Patterns. Figure [6](#page-5-1) shows the latency results for producer-consumer, client-server, and proxy patterns. Dep-TEE achieves a 7x increase in performance over Penglai-Copy for 8KB of data, and a 2000x increase for 2MB of data. Compared to Penglai-Remapping, Dep-TEE shows significant performance advantages for data sizes over 128KB, achieving a 10x increase for 2MB of data. This is because the overhead of unmapping and mapping operations in the remapping mechanism becomes extremely large as data size increases.

Performance of Multilateral Communication Patterns. Figure [7\(](#page-5-2)a) shows the performance of various systems under multibilateral communication patterns using the producer-consumer pattern. Dep-TEE significantly outperforms Penglai-Copy for transferring 1MB of data, achieving up to 3000x acceleration. Penglai-Copy and Penglai-Remapping fail with more than ten pairs due to PMP register limitations. Elasticlave faces the same constraints. In contrast, Dep-TEE supports over 100 pairs, demonstrating its potential for large-scale multitasking in cloud environments.

Dep-TEE's communication system is implemented on Penglai TEE. The Mounted Merkle Tree (MMT) ensures data and code integrity, while cache line locking prevents cache side-channel attacks. Additionally, segment-based registers for physical memory isolation can protect the enclave privacy from untrusted OS applications.

Mitigating Rowhammer Attacks. Rowhammer attacks exploit hardware flaws to induce bit flips in memory cells, potentially compromising page tables and altering memory mappings. Dep-TEE mitigates this threat by using the SPMP mechanism to decouple permission protection from page tables in a TEE region. This isolation ensures that even if a Rowhammer attack modifies the page tables, it prevents attackers from bypassing SPMP checks and unauthorized access to enclave data. Attackers might still corrupt data within the enclave but cannot extract sensitive information. Other defences against Rowhammer, like guard rows [\[14\]](#page-6-24), counter-based methods [\[20\]](#page-6-25), and ECC [\[6\]](#page-6-26), complement our approach and are orthogonal to our research.

Defending against TOCTTOU Attacks. TOCTTOU attacks exploit the window between the time of checking a resource and the time of using it, potentially allowing malicious enclaves to manipulate shared memory. Dep-TEE uses the SPMP mechanism to transfer shared memory ownership with transfer instruction, ensuring only one enclave can write to the shared memory at a time. This prevents TOCTTOU attacks, maintaining secure and consistent access to shared memory.

6 CONCLUSION

This paper presents Dep-TEE, a new TEE design that achieves scalable, high-performant, and secure inter-enclave communication with decoupled protection. Our evaluation shows that Dep-TEE significantly optimizes inter-enclave communication numbers, enhances communication performance and requires minimal extra hardware resources. In the future, we plan to extend Dep-TEE to support communication between enclaves in different TEE regions. Additionally, we aim to enable secure communication between the TEE and the Rich Execution Environment (REE).

Figure 8: Performance of benchmarks.

Figure [7\(](#page-5-2)b) shows the performance in a single-producer multiconsumer pattern, including data read/write latency. For 1MB data transfers, Dep-TEE outperforms Penglai-Copy and is slightly better than Penglai-Remapping. This is because the remapping mechanism doesn't perform multiple re-mappings and un-mappings in this pattern, avoiding overhead issues such as TLB shootdown. Dep-TEE manages large volumes of data by updating the value of SPMP registers. Penglai-Copy, Penglai-Remapping and Elasticlave do not support more than 20 consumer enclaves due to PMP register limitations. In contrast, Dep-TEE supports at least 100 consumer enclaves, making it suitable for cloud computing applications.

5.2 Benchmark suites

RV8 Benchmark Suite. We used the RV8 benchmark suite to evaluate compute-intensive workloads in environments with physical memory isolation. As shown in Figure [8\(](#page-5-3)a), the introduction of the SPMP mechanism in Dep-TEE results in negligible performance overhead for CPU-intensive applications compared to Penglai-PMP.

Redis Benchmark Suite. Redis, a widely used in-memory data store, evaluates the SPMP mechanism's impact on memory-intensive applications. Redis-benchmark simulates multiple client connections to a Redis server, measuring the average number of requests per second. Figure [8\(](#page-5-3)b) shows that the SPMP mechanism does not introduce significant overhead for the Nanhu V2 Core.

5.3 Hardware Costs

The Vivado [\[23\]](#page-6-23) resource utilization report (Table [4\)](#page-5-4) shows the impact of the SPMP mechanism on the FPGA. After implementing SPMP on the Xiangshan Nanhu V2 Core, we observed minimal Dep-TEE: Decoupled Memory Protection for Secure and Scalable Inter-enclave Communication on RISC-V ASPDAC '25, January 20–23, 2025, Tokyo, Japan

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